

# Dustin Kendall

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## Objective

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I am seeking a position as an electronics design engineer. I want to develop, design, and test efficient and innovative systems. I enjoy learning new technologies, adapting to new challenges, and putting my analytical thinking and math abilities to use daily.

## Education and Certifications

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**Bachelor of Science in Electrical Engineering** **Dec. 2019**  
*Candidate at Purdue Northwest GPA 3.97/4.0*

**Associates of Applied Sciences in Business Administration** **May 2016**  
*IvyTech Community College GPA 3.55/4.0*

## Experiences

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**ContiTech – Software Engineering Intern** **Sept 2018 – Current**

- Develops, tests, and debugs android applications in Java
- Develops, tests, and debugs graphical Python applications
- Works in Unix environment
- Designs and troubleshoots digital and analog circuits
- Develops C/C++ software for embedded Linux
- Uses Git to accomplish CI/CD
- Drafts UML diagrams
- Uses KiCad to draft ASIC designs
- Manages localization efforts

**Arcelor Mittal - Electrical Engineering Intern** **May 2018 – Aug 2018**

- Worked with a team to create a cooling tower specification
- Assisted in design of new systems with Allen Bradley PLCs

**DK Marketing – Web Marketer, Web Designer** **Sept 2013 – Sept 2018**

- Consulted clients about website needs
- Recommended plans of action and effective designs
- Coded and built websites and maintained virtual Linux servers
- Used Dreamweaver, Photoshop, Illustrator and other Adobe Suite products

**Continental Electric – Electrician's Apprentice** **Apr. 2013 – May 2014**

- Assisted in wiring industrial and commercial projects
- Installed power inverters along with transformers for solar panels

## Skills

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| <ul style="list-style-type: none"><li>• Microsoft Office Suite</li><li>• Computer Hardware</li><li>• Strong IT background</li><li>• Java</li><li>• Android</li><li>• Unix</li><li>• Windows</li><li>• GIT</li></ul> | <ul style="list-style-type: none"><li>• Python</li><li>• Troubleshooting Hardware and Software</li><li>• Circuit design</li><li>• Strong analytical skills and first principle thinking</li><li>• Strong communication skills</li><li>• JetBrains IDE's</li></ul> | <ul style="list-style-type: none"><li>• Object-Oriented Programming</li><li>• Strong math background</li><li>• VHDL Circuit Design</li><li>• Quartus Software</li><li>• FPGA Development</li></ul> |
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## Professional and Academic References

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Can be furnished upon request